



2823  
PATENT

Case Docket No. FUSO3.001AUS

Date: March 24, 2003

Page 1

In re application of : Yokouchi et al.  
App. No. : 10/007,054  
Filed : December 3, 2001  
For : WAFER OXIDATION  
REACTOR AND A METHOD  
FOR FORMING A  
SEMICONDUCTOR DEVICE  
Examiner : J. J. Maldonado  
Art Unit : 2823

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Sir:

Transmitted herewith in the above-identified application.

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Election  
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any

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Yokouchi, et al.	)	Group Art Unit 2823
Appl. No.	:	10/007,054	)	
Filed	:	December 3, 2001	)	
For	:	WAFER OXIDATION REACTOR AND A METHOD FOR FORMING A SEMICONDUCTOR DEVICE	)	
Examiner	:	J. J. Maldonado	)	

RESPONSE TO RESTRICTION REQUIREMENT

United States Patent and Trademark Office  
PO Box 2327  
Arlington, VA 22202

Dear Sir:

In response to the Restriction Requirement in the Office Action mailed from the United States Patent and Trademark Office on February 26, 2003, Applicants hereby elect Group I, consisting of Claims 1-12 and 27-31, drawn to a semiconductor manufacturing apparatus, classified in class 118, subclass 723R. This election is made without traverse.

CONCLUSION

The Applicant submits that the above election of Group I, now places the application in condition for allowance. If the Examiner has any questions which may be answered by telephone, he is invited to call the undersigned directly.

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Filed : December 3, 2001

Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 3/24/03

By:   
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